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Complete if Known

Application Number	10/004,441
Filing Date	5 December 2001
First Named Inventor	MAUNG, E.
Group Art Unit	---
Examiner Name	---
Attorney Docket Number	TR-083

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DG	1	Widmer, A.X., et al, "A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code", IBM Journal of Research and Development, Vol. 27, No. 5, September 1983.	Technology Center 2100
DG	2	ITU-T, Telecommunication Standardization Sector of ITU, Series V: Data Communication Over the Telephone Network (Error control) Error-correcting procedures for DCEs using asynchronous-to-synchronous conversion, ITU-T Recommendation V.42 (10/96).	

Examiner Signature	<i>[Signature]</i>	Date Considered	8/5/2004
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